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2004 IEEJ (7<sup>th</sup>)

## International Analog VLSI Workshop

University of Macau, Macao SAR, China, October 14 - 15, 2004

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The Research Committee on Electronic Circuits of the **Institute of Electrical Engineers of Japan (IEEJ)** is sponsoring and cooperating with **University of Macau, Chipidea Microelectronics (Macao) Limited** and **IEEE Macau Section** to hold the 2004 International Analog VLSI Workshop in Macao, on October 14-15, 2004. The purpose of the Workshop is to exchange information, ideas and recent research results on analog VLSI circuits and their applications. This Workshop provides an informal setting for a group of analog VLSI specialists from both industry and academia worldwide, to meet and brainstorm. The work does not have to be complete, e.g. results from a test chip are not mandatory.

The topics for the Workshop include but are not limited to:

- ◆ Amplifiers
- ◆ Comparators
- ◆ Voltage References
- ◆ A/D and D/A Converters
- ◆ Mixed Analog and Digital Circuits
- ◆ Analog Signal and Information Processing Applications (Telecommunication, Multimedia, Consumer, Medical, Automotive, Space, Military, etc.)
- ◆ Filters
- ◆ Multipliers
- ◆ Sample-And-Hold Circuits
- ◆ Consumer Electronics
- ◆ Analog Circuit Simulators
- ◆ RF and Microwave Circuits
- ◆ Analog Layout Techniques

A **Best Paper Award** will be offered by Chipidea Microelectronics (Macao) Limited.

<b>Author's Schedule:</b>	<b>Deadline for Submission of Extended Summary</b>	<b>June 30, 2004</b>
	Notification of Acceptance	August 1, 2004
	Deadline for Submission of Camera Ready Paper	August 23, 2004

Authors are requested to send an e-mail ([takakubo@elect.chuo-u.ac.jp](mailto:takakubo@elect.chuo-u.ac.jp)) or 4 copies of an extended summary up to **3 pages** together with information including the complete title, author name(s), affiliation(s) of author(s), and corresponding author (name, postal and e-mail addresses, telephone/fax numbers) to the following Secretariat by **June 30, 2004**. Notifications of acceptance will be sent by e-mail by **August 1, 2004**. The official language is English only. The deadline for camera-ready manuscript is **August 23, 2004**. It is mandatory that the accepted paper should be presented at the Workshop.

### Paper submit to TPC Secretary:

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**Keynote Speeches** addressed by experienced leading experts from **Analog and Mixed-Signal ICs** industry (one from **Japan**, one from **Europe** and one from **USA**).

(The details will be announced soon in the Workshop Main Page)

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